

7 April 1980

MEMORANDUM FOR THE RECORD

SUBJECT: ARS FASTCAST/HighSpeed Configurations

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1. A meeting was held at [] on April 4, 1980 to redefine the requirements for ARS stand-alone operation and to further coordinate the activation schedule. It was mutually agreed that if part of the Phase II requirements, ie (courtesy copy and spills be altrouted to ARS) can be completed with minimum effort that ARS will be activated at the completion of Phase I. Testing will begin approximately 1 June 1980 and the software will be ready 30 Jun 1980. Activation will take place in early July if all of the above testing is satisfactorily completed.

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2. [] expressed concern for software support and requested that an ARS programmer be on site for the early July activation date.

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3. [] requested that [] circuit requirements be sent to him NLT 15 May. [] OPS agreed to have the above information available for the 1 May meeting which has been scheduled for review of plans and progress. The meeting will take place at []

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4. Attached is a detailed discussion of the hardware requirements for the ARS at []

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[]
D/Chief, Engineering

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HARDWARE NOTES FROM 4 APRIL 80 ARS MEETING

25X1 1. A 2400BPS and possibly 4800 BPS MTU to LTU (B-10) trunk will serve as the communications link between MAX III and ARS II. Back-up for this link will be in form of normally unused MTU's and LTU's (B-10) such as WASH, ARS II to ARS V trunk or ARS II to MAX II trunk which can be patched at to form a MAX III TO ARS II trunk.

2. Establishing a "virtual" trunk between MAX III and ARS II, dedicated to passing CRT and Line Printer data, will be worked out during Phase II. In the interim^{EL}, after activation of ARS II, the "spills" and "courtesy copies" in MAX III will simply be alt-routed to ARS II via the normal trunk. Some discussion about the possibility of a loop developing between the systems will be investigated. ARS must detect the same spill error as MAX or ARS will return the message to MAX and cause a circle.

3. Up to 20 "Fastcast" and "Modem Emulator" (high speed) circuits and an undetermined number of low speed's are to be activated during Phase I. It was emphasized that there was some uncertainty in the actual number of 2400 BPS circuits ARS could simultaneously support. The probability that a staggered service would all for the use of 20 LTU's (B-10) was discussed. The final answer lies in actual testing.

4. ARSR and ARSO will be alt-routed to MAXR and MAXO. This will eliminate the need for two teletype terminals being located in the operating area.

5. The removal of one MAX Highspeed Line Printer upon activation

of Phase I will allow for the installation of the two ARS Medium Speed Line Printers in the operating area of the [REDACTED]

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6. ARS II Supervisory Control Console and Message Control Console will be located in the operating area in the vicinity of the MAX III control positions. Silent 700 teleprinters, operating at 300 BPS ASCII, will be installed.

7. The third Medium Speed Line Printer, which was a part of MAXCON, will be removed from the ARS bus configuration, but maintained as a spare (located in the equipment room of the [REDACTED])

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8. The MAXCON pseudo LTU rack and associated line switch will be left in the ARS bus configuration for the time being so the programmers can determine its usefulness in software development.

9. [REDACTED] will make LTU location/assignment information available to the programmers as soon as possible. All due care will be taken to establish a proper distribution of Control, Monitor, Synchronous High Speed, "Fastcast" and Low Speed circuits so failure of any shelf within the LTU cabinet will not totally disable the system.

10. A Superbee III will be used as ARS II High Speed software monitor at 2400 BPS ASCII asynchronous, serviced from a B-8A LTU. This is to be implemented during Phase II. This High Speed Monitor may possibly serve as MAX III High Speed Monitor via the "virtual" circuit.

11. Some discussion as to the possibility of using Silent 700's at 300 BPS for Low Speed Monitors was left up in the air. The programmers will check into the problem of Baudot to ASCII code conversion. [REDACTED] desires at least two 300 BPS Monitors.

12. The B-6 LTU's exchanged with MAX 1A, which do not have

redundant addressing logic, were briefly discussed with the subject deferred to COMSEC.

13. The MAXCON Record Channel Interface cards will be removed from the ARS II bus configuration. It is possible these are the same cards used in DATEX.

14. The MAXCON Superbee III CRT's may be used with ARS II Spill positions. This matter to be investigated jointly by the Technician and HQ programmers prior to activation. In either case B-8A LTU's will be used.

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